

Docket No.: M4065.0181/P181-B  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Leonard Forbes

Application No.: Not Yet Assigned

Group Art Unit: 2816

Filed: 25 JUL 2003

Examiner: Q. Tra

For: INTEGRATED CIRCUIT AND METHOD  
FOR MINIMIZING CLOCK SKEWS

**REQUEST FOR APPROVAL OF DRAWING CORRECTIONS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Applicant hereby requests Examiner approval of the drawing corrections shown in red on the attached set (seven sheets, figures 1A-11B) of formal drawings submitted herewith for filing in the above-identified patent application.

Favorable consideration of the enclosed corrected drawings is respectfully requested.

Dated: 25 JUL 2003

Respectfully submitted,

By 

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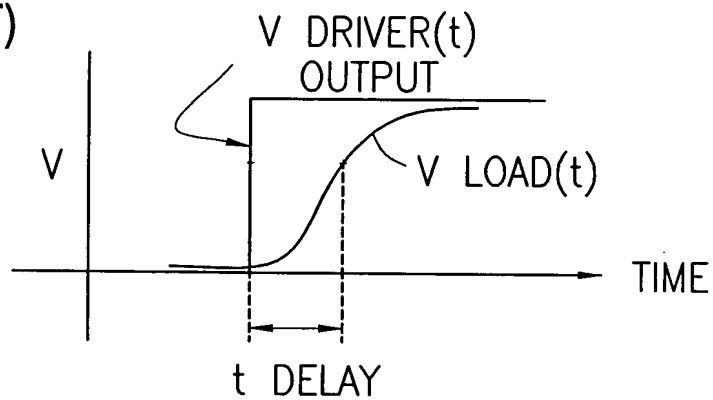
2101 L Street NW

Washington, DC 20037-1526

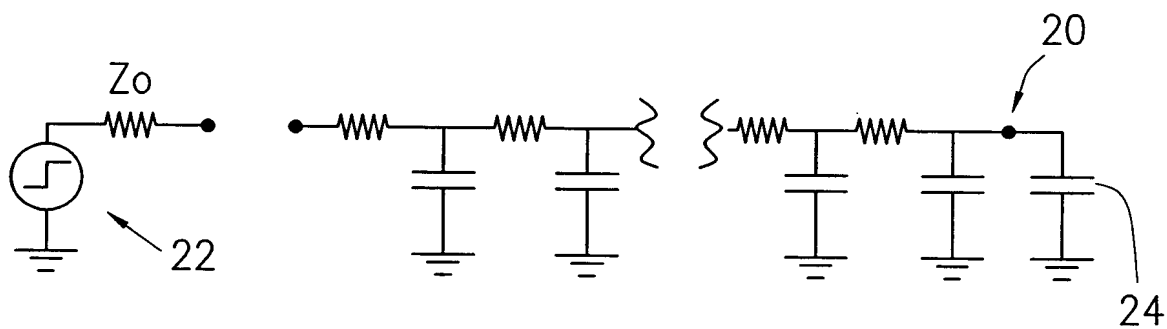
(202) 785-9700

Attorneys for Applicant

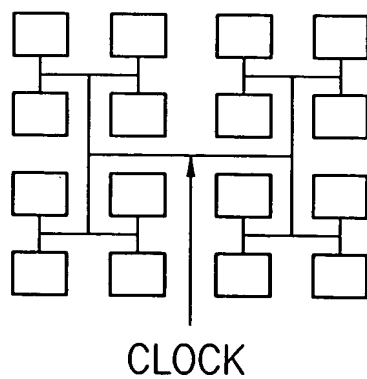
**FIG. 1A**  
(PRIOR ART)



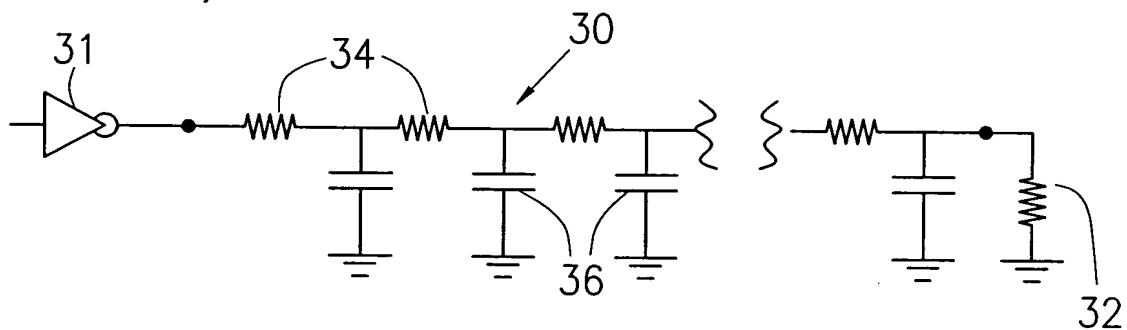
**FIG. 1B**  
(PRIOR ART)



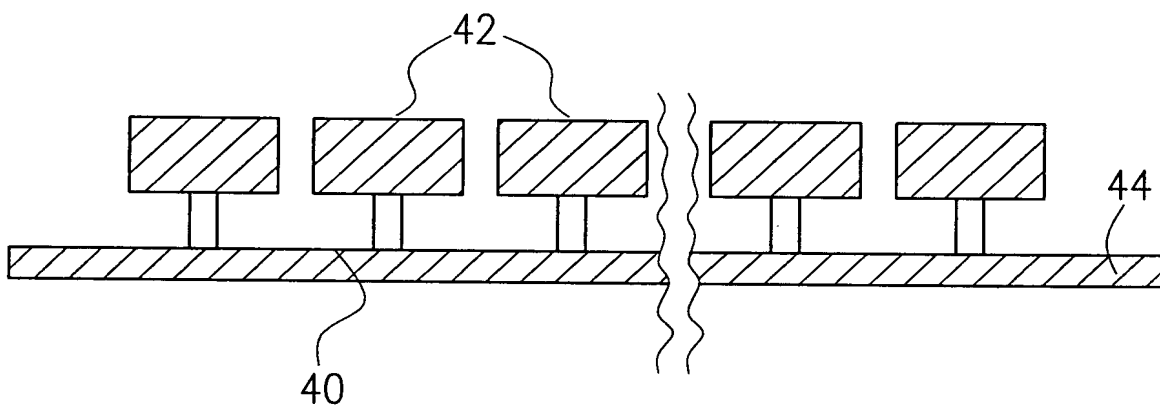
**FIG. 2**  
(PRIOR ART)



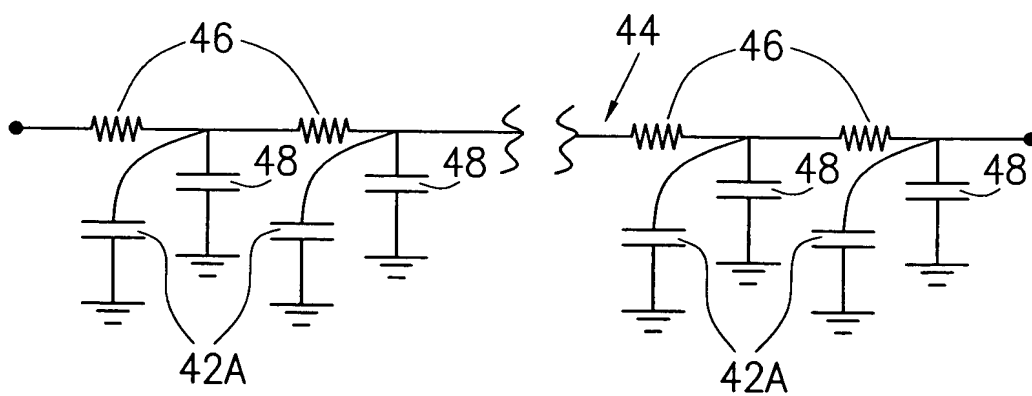
**FIG. 3**  
(PRIOR ART)



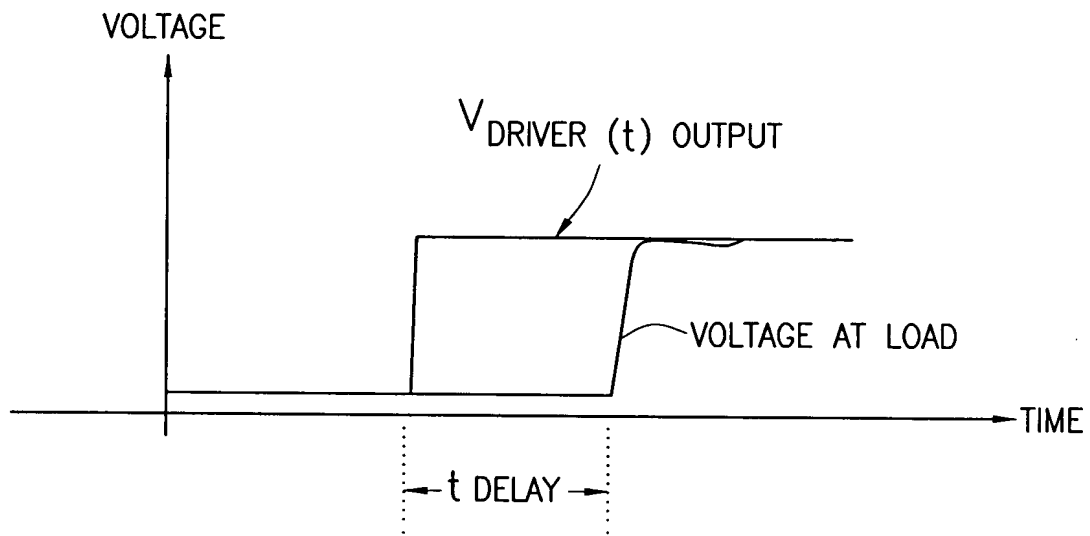
**FIG. 4**



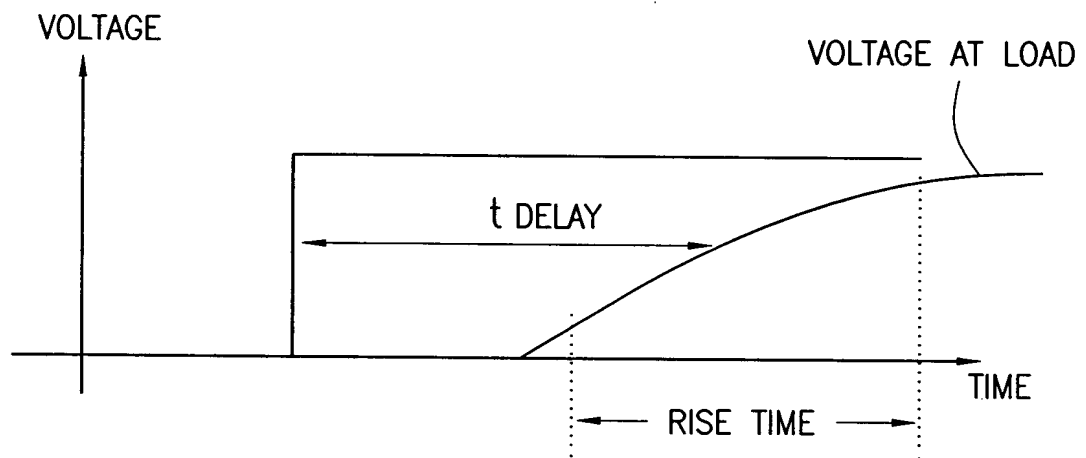
**FIG. 5**



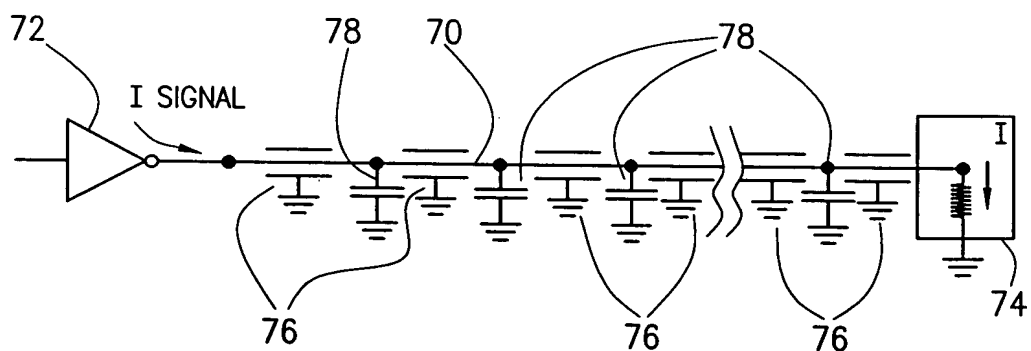
**FIG. 6A**



**FIG. 6B**



**FIG. 7**





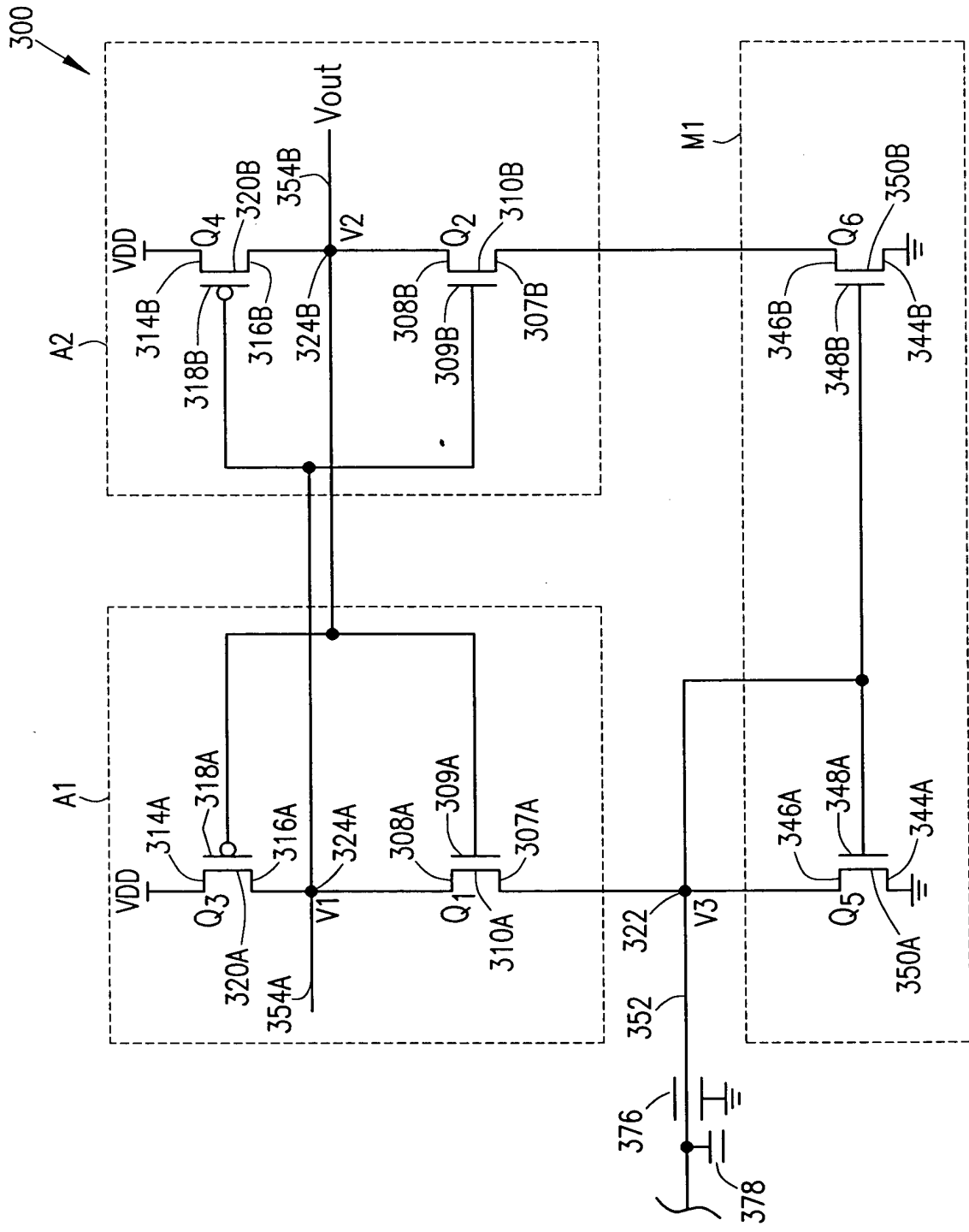
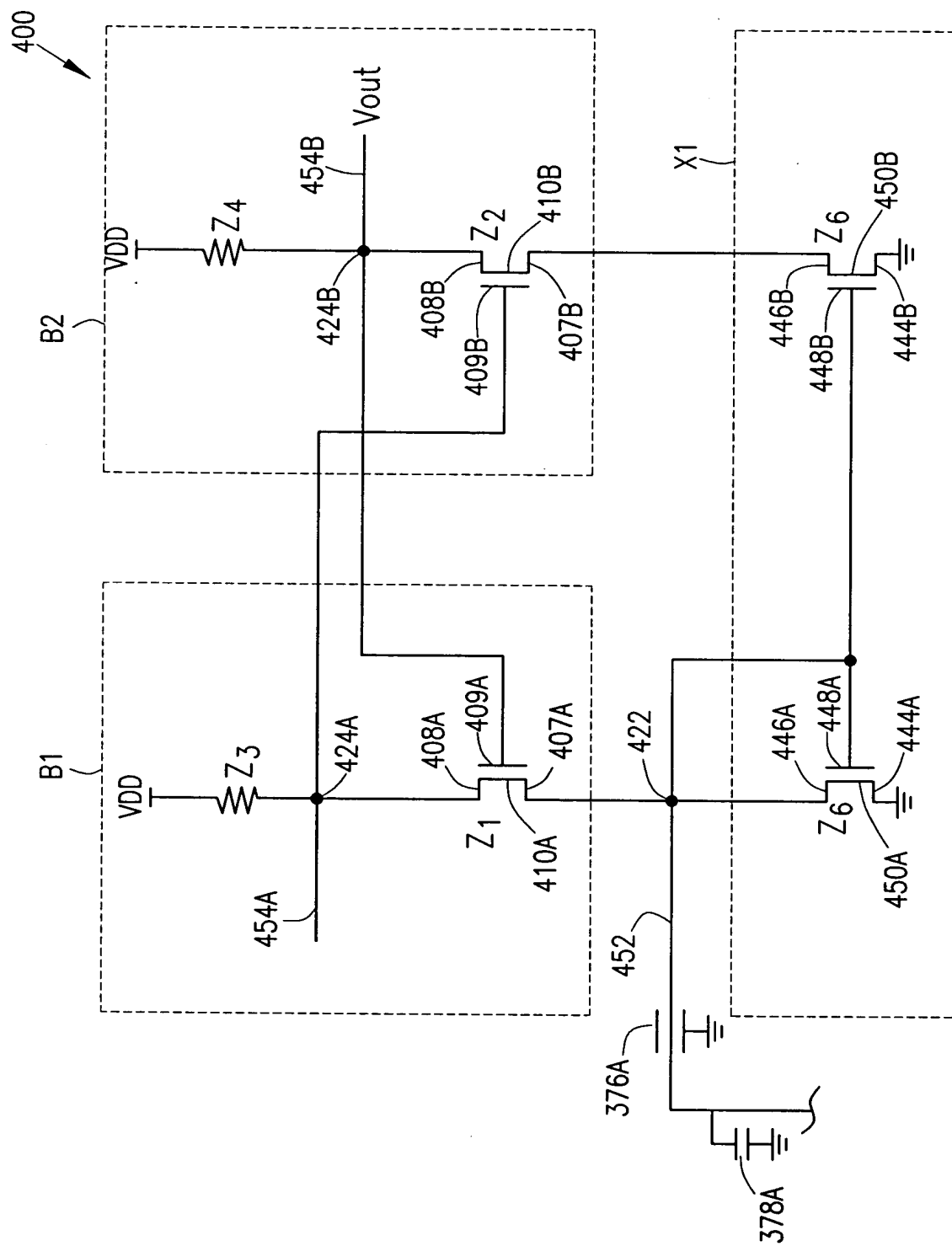
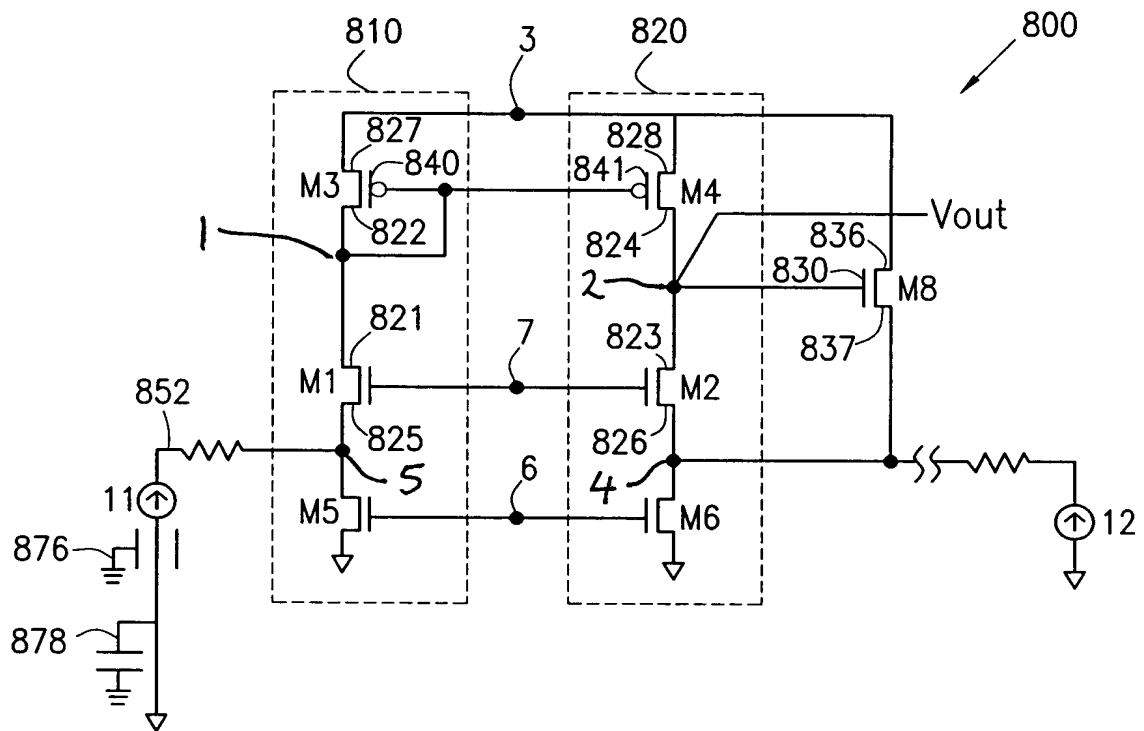


FIG. 9



**FIG. 11A**



**FIG. 11B**

